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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/748,931	12/30/2003	Jung-Hoon Oh	SAM-0535	1934
7590		02/08/2005	EXAMINER	
Anthony P. Onello, Jr.		DUVERNE, JEAN F		
MILLS & ONELLO LLP				
Suite 605		ART UNIT		
Eleven Beacon Street		2839		
Boston, MA 02108		PAPER NUMBER		
		DATE MAILED: 02/08/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/748,931

Applicant(s)

OH ET AL.

Examiner

Jean F. Duverne

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 30 December 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 16 is/are allowed.
- 6) ☐ Claim(s) 1-5, 8 and 10-13 is/are rejected.
- 7) ☒ Claim(s) 6, 7, 9, 14 and 15 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 12/30/03.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-5, 8, 10-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Selinko (US patent 4,437,718) in view of Dube (US patent 4,750,890).

Selinko's device discloses socket assembly for an integrated circuit (50), comprising: a guide block at 30 provided in a terminal region of a board (10), said guide block defining an area into which an integrated circuit (50) can be inserted opposite terminals formed on the board the terminal region; a guide part at 57 provided on an inner side wall of the guide block, to guide an insertion position of the integrated circuit so that respective leads the integrated circuit are aligned with the corresponding terminals of the test board; and a pressurizing plate at 36 adapted to interface with the guide block, the pressurizing plate including a pressurizing protrusion at 20 on a surface thereof used as aligning features, such that when the pressurizing plate is applied to the guide block, the respective leads of the integrated circuit are urged to connect with the corresponding terminals of the board; wherein the guide block is shaped to include an insertion hole at 37, through which the terminal region the board is exposed and in which

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the integrated circuit can be inserted; and wherein the guide part provided on an inner wall of the insertion hole, and configured as a protrusion to mate with a mating protrusion (55) on a side portion of the integrated circuit so as to guide the horizontal position of the integrated circuit during vertical insertion (see fig. 3); wherein the guide block comprises at least two independent separated sidewalls positioned in the terminal region of the test board, and wherein the guide part is configured at the sidewall of the guide block as a protrusion to mate with a mating protrusion on side portion of the integrated circuit so as guide the horizontal position of the integrated circuit during vertical insertion ((see fig. 3). However, Selinko's device fails to explicitly disclose the adapter with the testing capability. Dube's device discloses the use of an adapter test socket. It would have been obvious to one having ordinary skill in art at the time the invention was made use the adapter test socket such the one disclosed in Dube's device in order to have the capability for testing Selinko's package after initial installation for failure.

Conclusion

3. Claims 6-7, 9, 14-15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Prior art fails to disclose the pressuring plate with the alignment the elastic member; plurality of needles, the upper and lower ends of which pierce upper and lower surfaces of the needle block to connect the integrated circuit leads and the board; the

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second pressurizing plate adapted to replace the first pressurizing plate, the second pressurizing plate including the pressurizing protrusion on a surface thereof, such that when the second pressurizing plate is applied to the needle block, the respective leads of the integrated circuit are urged to connect with the underlying needle upper ends; the guide block further comprises the elevating part, said elevating part being adapted to support and elastically elevate an inserted integrated circuit; the integrated circuit having the guide member mating with the vertical flute or protrusion with rest of the claims limitations.

Claim 16 is allowed (see the aforementioned allowable subject matter.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean F. Duverne whose telephone number is (571) 272-2091. The examiner can normally be reached on 9:00-7:30, Monday-Thursday.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TC Patel can be reached on (571) 272-2098. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JFD

02/03/2005



Jean Frantz Duverne
Primary Examiner
Art Unit 2839